



TCXO & VCTCXO SMD5032 (CMOS Level Output)



4 Pad Version 5.0×3.2 mm

- $\pm 0.5 \sim \pm 2.5$ ppm stability
- CMOS Level Output
- Low power consumption
- Reflow soldering temperature: 260°C max

RoHS
2002/95/EC

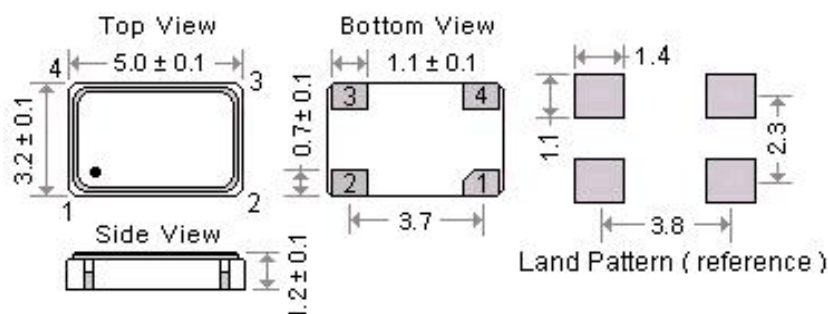
RoHS compliant

★ PARAMETERS

PARAMETERS	SPECIFICATION			
Frequency Range	6.4~52.0MHz			
Frequency Tolerance	±1.0ppm、±2.0ppm at +25± 2℃			
Frequency Stability	Vs Temperature (ref. to +25℃)		±0.5~ ±2.5ppm	
	Vs Supply voltage change (±5%)		±0.3ppm	
	Vs Load change (±10%)		±0.3ppm	
	Vs Aging (1 st Year , 25±2℃)		±1.0ppm	
Supply Voltage	1.8V、2.5V、2.8V、3.0V、3.3V			
Output Waveform	Square Wave			
Output Load	CMOS 15pF			
Current Consumption	6.0 mA max.			
Pad "1" Function	GND or E/D			
Output Level	Logic High "1": 90% V _{DD} min.			
	Logic High "0": 10% V _{DD} max.			
Operating Temp. Range	-30~+85℃、-40~+85℃			
Storage Temp. Range	-55~+125℃			
Electrical Frequency Tuning (For VCTCXO)	Control Voltage Center	1.8V	2.5V/2.8V	3.0/3.3V
		0.9±0.6V	1.4±1.0V	1.5±1.0V
	Frequency Control Range	±5.0ppm min.		
Start-up Time	2.0mS typ., 5.0mS max.			
Phase Noise	-122 dBc/Hz max @100Hz offset			
	-138 dBc/Hz max @1KHz offset			
	-145 dBc/Hz max @10KHz offset			

⊙ All specification subject change without notice.

★ DIMENSIONS& LAND PATTERN LAYOUT (Unit: mm)



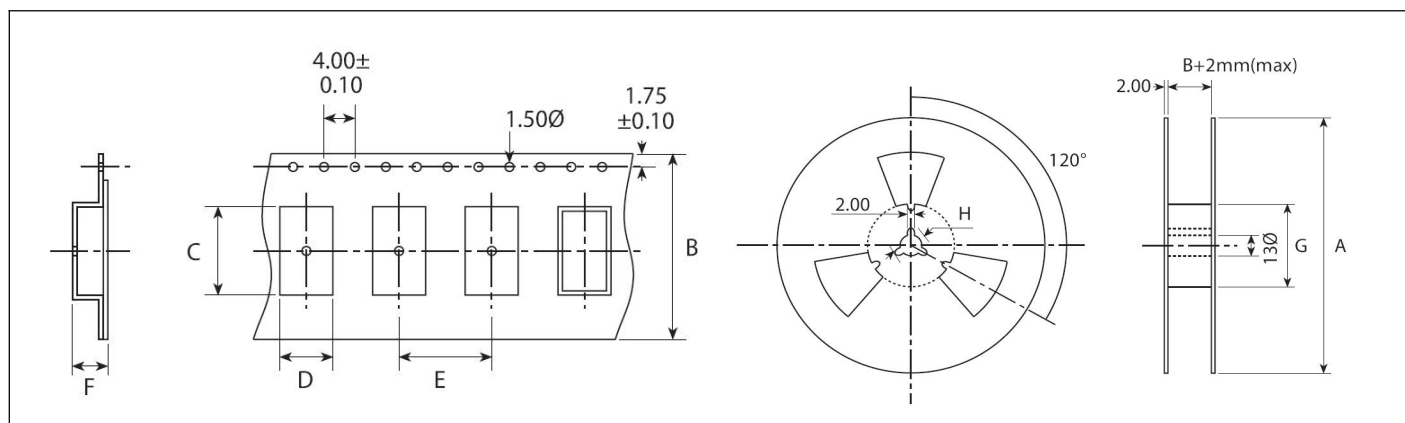
Pin	Connection
1	GND or E/D (TCXO) V _{CON} (VCTCXO)
2	GND
3	Output
4	V _{DD}

**★ PART NUMBER GUIDE** *e.g. FTO26.000M3.0SM5T-1.0/2.5C (*SM5T=5.0×3.2 SMD TYPE, CMOS, 1# GND)*

Logo	Type	Frequency	Supply Voltage	Package	Output Waveform	Frequency Tolerance	Frequency Stability	Operating Temp.
F	TO	26.000M	3.0	SM5	T or T1	1.0	2.5	C
	VTO				T			

★ TEST CIRCUIT

TCXO	VCTCXO
$L_{CMOS}=15pF$ (Total Fixture and Probe Capacitance) Note: PIN#1: O/E available	$L_{CMOS}=15pF$ (Total Fixture and Probe Capacitance)

★ TAPING SPECIFICATION (Unit: mm)

	A	B	C	D	E	F	G
TCXO-SMD5032	178±2.0	12.0±0.3	5.40±0.10	3.60±0.10	8.0±0.1	1.7±0.2	60.5±1.0
1000 pcs per reel							