MCS1800

3.3V, Linear Hall-Effect Current Sensor with ±3% Accuracy Over Temperature

DESCRIPTION

The MCS1800 is a linear Hall-effect current sensor for AC or DC current sensing. The Hall array is differential, which cancels out any stray magnetic field.

A primary conductor with a low resistance allows current to flow close to the IC, which contains high-accuracy Hall-effect sensors. This current generates a magnetic field that is sensed at two different points by the integrated Hall-effect transducers. The magnetic field difference between these two points is then converted into a voltage that is proportional to the applied current. A spinning current technique is used for a low stable offset.

The galvanic isolation between the pins of the primary conductive path and the sensor leads allows the MCS1800 to replace opto-isolators or other isolation devices.

The MCS1800 requires a minimal number of readily available, standard external components. The device's small footprint saves board area and makes it well-suited for space-constrained applications. The MCS1800 is available in an SOIC-8 package.

FEATURES

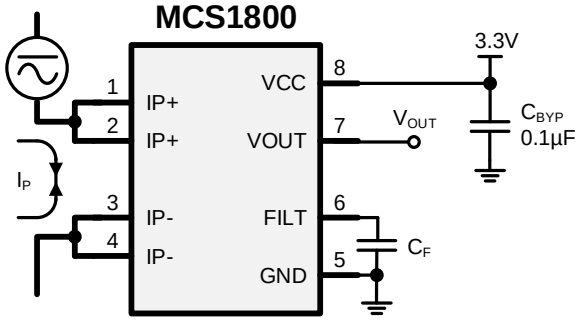
- 3.3V Single Supply
- Immune to External Magnetic Fields by Differential Sensing
- 200V_{RMS} Working Voltage for Basic Isolation
- Operating Temperature: -40°C to +125°C
- 1.2mΩ Internal Conductor Resistance
- ±12.5A and ±25A Range
- Adjustable Bandwidth Up to 100 kHz
- 4µs Output Rising Time
- Ratiometric Output from Supply Voltage
- Output Proportional to AC or DC Currents
- Factory-Trimmed for Accuracy
- No Magnetic Hysteresis
- Integrated Shield Suppressing Capacitive Coupling from Current Conductor to Die (Up to 10V/ns)
- Available in an SOIC-8 Package

APPLICATIONS

- Motor Control
- Automotive Systems
- Load Detection and Management
- Switch-Mode Power Supplies
- Over-Current Fault Protection

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TYPICAL APPLICATION



Note:

1) V_{OUT} is proportional to I_P within the specified range. The noise vs. bandwidth tradeoff can be adjusted by connecting a capacitor (C_F) between FILT and GND.

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ORDERING INFORMATION

Part Number*	Optimized Primary Current Range(A)	Typ. Sensitivity (SENS) (mV/A)	Top Marking	MSL Rating
MCS1800GS-12	±12.5	110	MC180012	1
MCS1800GS-25	±25	55	MC180025	

* For Tape & Reel, add suffix –Z (e.g. MCS1800GS-12–Z).

TOP MARKING (MCS1800GS-12)

MC180012
LLLLLLLL
MPSYWW

MC180012: Part number
LLLLLLLL: Lot number
MPS: MPS prefix
Y: Year code
WW: Week code

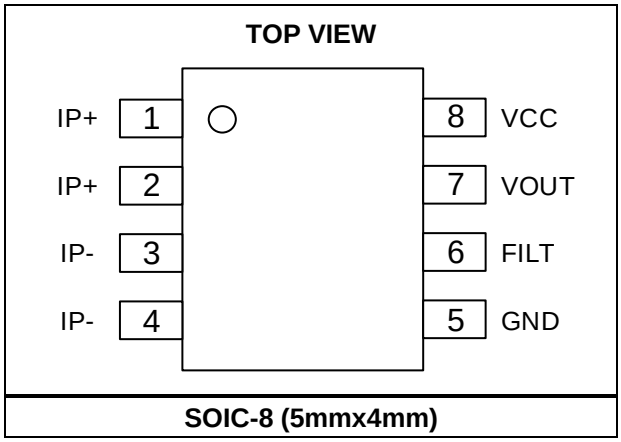
TOP MARKING (MCS1800GS-25)

MC180025
LLLLLLLL
MPSYWW

MC180025: Part number
LLLLLLLL: Lot number
MPS: MPS prefix
Y: Year code
WW: Week code

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PACKAGE REFERENCE



SOIC-8 (5mmx4mm)

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PIN FUNCTIONS

Pin #	Name	Description
1, 2	IP+	**Primary current (+).** IP+ is a terminal for the current being sampled. These pins are internally connected together.
3, 4	IP-	**Primary current (-).** IP- is a terminal for the current being sampled. These pins are internally connected together.
5	GND	**Ground.** Signal ground terminal.
6	FILT	**Filter.** FILT is a terminal for the external capacitor (C_F), which sets the bandwidth. FILT can be left floating when bandwidth setting is not required.
7	VOUT	**Analog output.**
8	VCC	**Voltage supply.** Connect VCC to a 3.3V power supply.

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ABSOLUTE MAXIMUM RATINGS [2]

Supply voltage (V_{CC}) .....-0.1V to +6V
Output voltage (V_{OUT}).....-0.1V to +6V
V_{FILT}.....-0.1V to +6V
Junction temperature .....165°C
Lead temperature .....260°C
Storage temperature..... -65°C to +165°C

ESD Ratings

Pin 6, Pin 7, and Pin 8
Human-body model (HBM) ..... ±2000V

Pin 5, Pin 6, Pin 7, and Pin 8
Charge device model (CDM) ..... ±2000V

Recommended Operating Conditions [3]

Supply voltage (V_{CC}) ..... 3.0V to 3.6V
Operating junction temp (T_J).....-40°C to +125°C

Notes:

2) Exceeding these ratings may damage the device.
3) The device is not guaranteed to function outside of its operating conditions.

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ISOLATION CHARACTERISTICS

Parameters	Symbol	Condition	Rating	Units
Withstand isolation voltage	V_{ISO}	Agency type-tested for 60 seconds in accordance with IEC62368-1. 100% Production tested at 1.2 x V_{ISO} for 1 second.	1000	V_{RMS}
Maximum isolation working voltage	V_{IOWM}	Maximum approved working voltage, according to IEC62368-1.	280	V_{PK}
			200	V_{RMS} or V_{DC}
External clearance	CLR	Shortest distance through the air from the IP leads to the signal leads.	4.2	mm
External creepage	CPG	Shortest distance along the package body from the IP leads to the signal leads.	4.2	mm

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MCS1800 COMMON ELECTRICAL CHARACTERISTICS [4]

Typical values are at V_{CC} = 3.3V, C_F = 0nF, C_L = 1nF, T_J = -40°C to +125°C, unless otherwise noted.

Parameters	Symbol	Condition	Min	Typ	Max	Units
Supply voltage	V_{CC}		3.0		3.6	V
VCC under-voltage lockout threshold	VCC_{UVLO}	V_{CC} rising	2	2.5	3	V
VCC under-voltage lockout hysteresis	VCC_{UVLO_HYS}			400	750	mV
Operating supply current	I_{CC}	V_{CC} = 5V		8.5	10.5	mA
Output capacitance load [10]	C_L	From VOUT to GND			10	nF
Output resistive load [10]	R_L	From VOUT to GND	4.7			kΩ
Primary conductor resistance [12]	R_P	Effective		1.2		mΩ
Frequency bandwidth [10]	f_{BW}	FILT disconnected		100		kHz
Internal filter resistance [9]	R_{Fi}			1.5		kΩ
Internal filter capacitance [8]	C_{Fi}			1		nF
Power on time [10]	t_{PO}	I_P = I_{PMAX}, FILT disconnected		90		µs
Rise time [10]	t_R	I_P = I_{PMAX}, FILT disconnected		4		µs
Propagation delay [10]	t_{PD}	I_P = I_{PMAX}, FILT disconnected		1.5		µs
Response time [10]	$t_{RESPONSE}$	I_P = I_{PMAX}, FILT disconnected		5		µs
Noise density [10]	I_{ND}	Input referred noise density		200		$µA_{(RMS)}$/ √Hz
Noise [10]	I_N	Input referred, 1nF on FILT (60kHz Bandwidth)		50		$mA_{(RMS)}$
Nonlinearity [10]	E_{LIN}	Over full range of I_P		0.5		%
Ratiometry	K_{SENS}	V_{CC} = 4.5 to 5.5V	97.5	100	102.5	%
	K_{V0}	V_{CC} = 4.5 to 5.5V, I_P = 0A	99	100	101	%
Zero current output voltage	$V_{OUT(Q)}$ (I_P = 0)	I_P = 0A		V_{CC} / 2		V
First Hall magnetic coupling factor [9]	P_{MCF1}		1.1	1.2	1.3	mT/A
Second Hall magnetic coupling factor [9]	P_{MCF2}		0.60	0.65	0.70	mT/A
Hall plate matching [10]	M_H			±1		%
Saturation voltage [5]	$V_{OUT(H)}$	R_L = 4.7kΩ, T_J = 25°C	V_{CC} - 0.3			V
	$V_{OUT(L)}$	R_L = 4.7kΩ, T_J = 25°C			0.3	V

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MCS1800-12 PERFORMANCE CHARACTERISTICS [4]

V_{CC} = 3.3V, C_F = 0nF, C_L = 1nF, T_J = -40°C to +125°C, unless otherwise noted.

Parameters	Symbol	Condition	Min	Typ [11]	Max	Units
Optimized accuracy range [6]	I_P		-12.5		+12.5	A
Sensitivity	SENS	-12.5A ≤ I_P ≤ +12.5A, T_J = 25°C		110		mV/A
Sensitivity error	E_{SENS}	I_P = 12.5A, T_J = 25°C to 125°C	-2.5		+2.5	%
		I_P = 12.5A, T_J = -40°C to +25°C		±2		%
Offset voltage [7]	V_{OE}	I_P = 0A, T_J = 25°C to 125°C	-15		+15	mV
		I_P = 0A, T_J = -40°C to +25°C		±10		mV
Total output error [8]	E_{TOT}	I_P = 12.5A, T_J = 25°C to 125°C	-3		+3	%
		I_P = 12.5A, T_J = -40°C to +25°C		±2.5		%
Sensitivity error lifetime drift [10]	$E_{SENS(D)}$			±1		%
Total output error lifetime drift [10]	$E_{TOT(D)}$			±1		%

MCS1800-25 PERFORMANCE CHARACTERISTICS [4]

V_{CC} = 3.3V, C_F = 0nF, C_L = 1nF, T_J = -40°C to +125°C, unless otherwise noted.

Parameters	Symbol	Condition	Min	Typ [11]	Max	Units
Optimized accuracy range [6]	I_P		-25		+25	A
Sensitivity	SENS	-25A ≤ I_P ≤ +25A, T_J = 25°C		55		mV/A
Sensitivity error	E_{SENS}	I_P = 25A, T_J = 25°C to 125°C	-2.5		+2.5	%
		I_P = 25A, T_J = -40°C to +25°C		±2		%
Offset voltage [7]	V_{OE}	I_P = 0A, T_J = 25°C to 125°C	-15		+15	mV
		I_P = 0A, T_J = -40°C to +25°C		±10		mV
Total output error [8]	E_{TOT}	I_P = 25A, T_J = 25°C to 125°C	-3		+3	%
		I_P = 25A, T_J = -40°C to +25°C		±2.5		%
Sensitivity error lifetime drift [10]	$E_{SENS(D)}$			±1		%
Total output error lifetime drift [10]	$E_{TOT(D)}$			±1		%

Notes:

4) See the Operation section on page 12 for more details.
5) Beyond the maximum specified current range (I_P), the current sensor continues to provide an analog output voltage proportional to the primary current until the device reaches the high or low saturation voltage. However, the nonlinearity increases beyond the specified range (I_P).
6) The device can operate at higher primary current levels (I_P) and ambient temperatures (T_A), as long as the device does not exceed the maximum junction temperature (T_J (MAX)).
7) The offset voltage does not incorporate any error due to external magnetic fields.
8) Percentage of I_P, when $I_P = I_{P_MAX}$. The output is filtered.
9) Guaranteed by design.
10) Guaranteed by characterization.
11) Typical values denoted with the "±" sign signify ±3 sigma values.
The resistance is defined as the total resistance measured from a point of the lead next to the solder joint, assuming that the two IP+ pins (and IP- pins) have the same potential (see Figure 1). This definition corresponds to the effective resistance used to estimate the Joule heating, calculated with R x I_P^2.

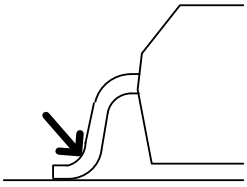


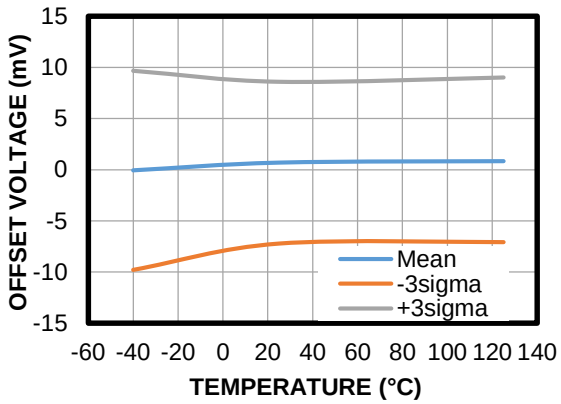
Figure 1: Total Resistance

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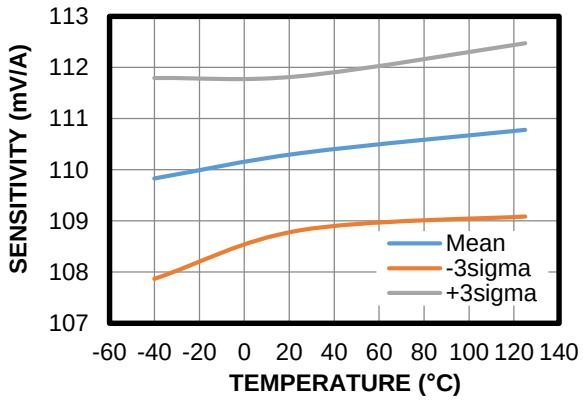
TYPICAL CHARACTERISTICS

MCS1800GS-12, unless otherwise noted.

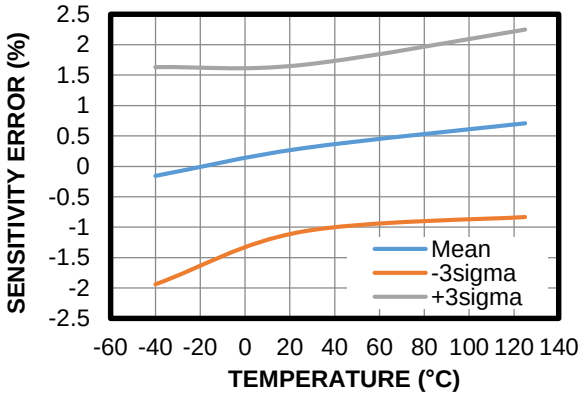
Offset Voltage vs. Temperature



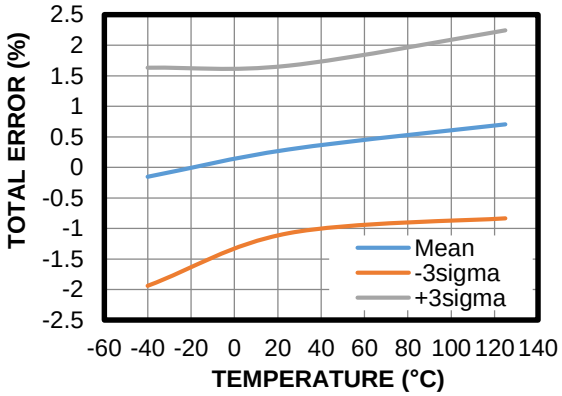
Sensitivity vs. Temperature



Sensitivity Error vs. Temperature



Total Error vs. Temperature

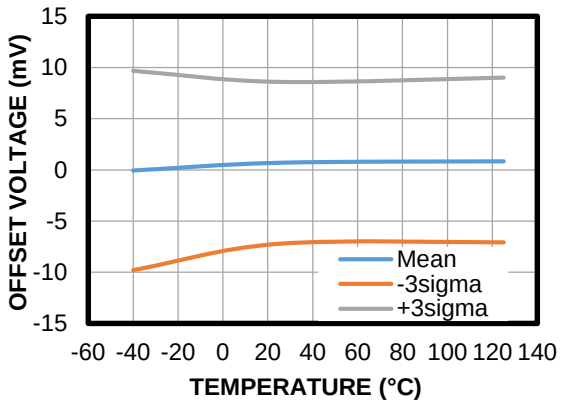


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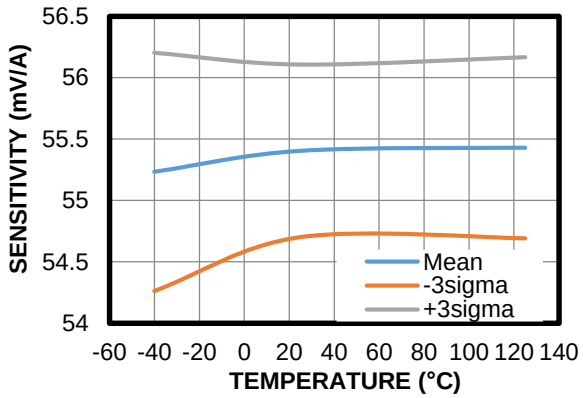
TYPICAL CHARACTERISTICS

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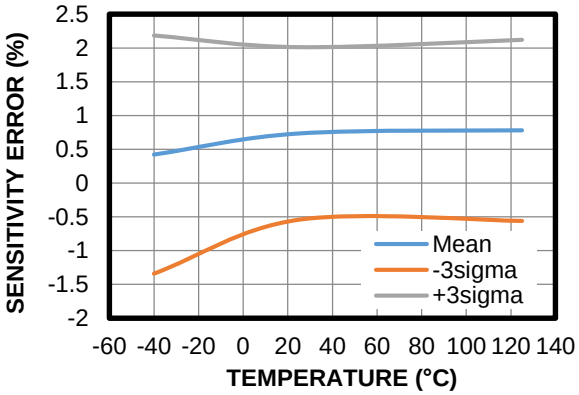
Offset Voltage vs. Temperature



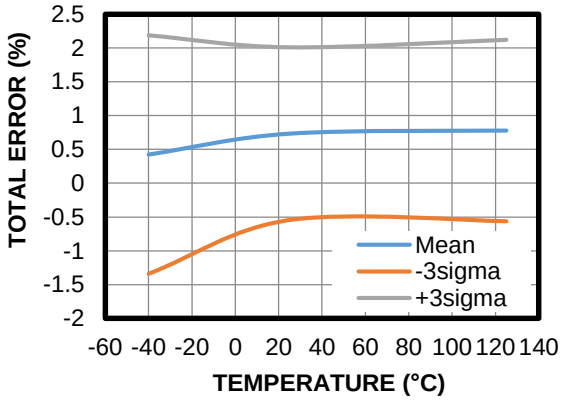
Sensitivity vs. Temperature



Sensitivity Error vs. Temperature



Total Error vs. Temperature



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FUNCTIONAL BLOCK DIAGRAM

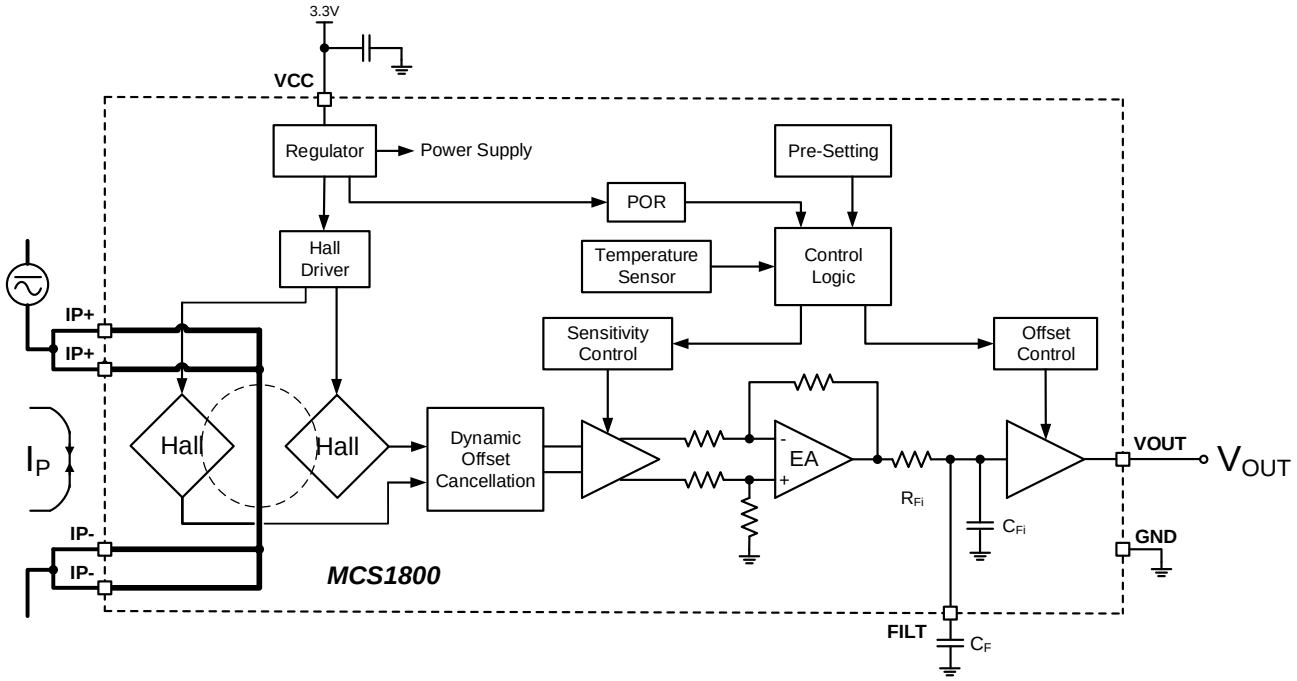


Figure 2: Functional Block Diagram

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OPERATION

Current Rating

I_{PMAX} is the rated current. The sensor output is linear, as a function of the primary current (I_P). I_{PMAX} follows the specified performances when I_P is between -I_{PMAX} and + I_{PMAX} (see Figure 3).

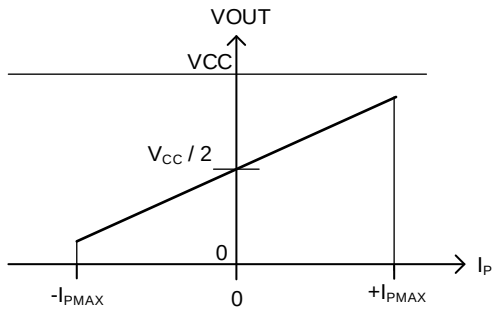


Figure 3 : Sensor Output Function

Sensitivity (SENS)

The sensitivity (SENS) (in mV/A) indicates how the output changes when the primary current changes. SENS the product of the average between the two coupling constants (P_{MCF1} and P_{MCF2}) (in mT/A) and the transducer gain (in mV/mT). The gain is factory-trimmed to the sensor target sensitivity.

Coupling constants (P_{MCF1} and P_{MCF2})

Figure 4 shows a cross-section of the sensor. The first and second Hall magnetic coupling factors are defined as the amount of the vertical magnetic field (B_1 and B_2) produced at the sensing points 1 and 2, per unit of current injected in the primary conductor. Due to the asymmetrical shape of the primary conductor, the magnetic field generated in the two sensing points are different.

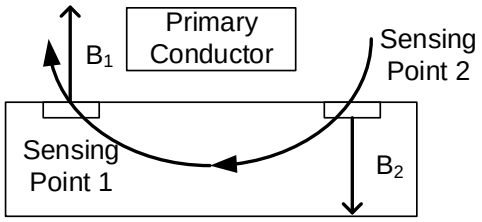


Figure 4: Cross Section of the Sensor

Noise (V_{NOISE})

The noise (V_{NOISE}) is a random deviation that cannot be removed by calibrating the device. The input's referred noise is the root mean square of the sensor's output noise (in mV) divided by the sensitivity (in mV/A). V_{NOISE} represents the smallest current that the device is able to resolve without any external signal treatment (the resolution is typically 3 times the RMS noise).

Other deviations are systematic, which means that they represent the average deviation across a large number of data points. These deviations can be removed by calibrating the device.

Zero Current Output Voltage ($V_{OUT(Q)}$)

$V_{OUT(Q)}$ is the voltage output when the primary current is 0A. The nominal value is V_{CC} / 2. The variation in $V_{OUT(Q)}$ from the nominal value is due to thermal drift, as well as the factory's resolution limits related to voltage offset trimming.

Offset Voltage (V_{OE})

The offset voltage (V_{OE}) is the difference between V_{CC} / 2 and the zero current output voltage. To convert this voltage into amperes, divide V_{OE} by the sensitivity.

Nonlinearity (E_{LIN})

The primary current and sensor output should have a linear relationship, indicated by a straight line. A line that is not straight indicates nonlinearity, which is a deviation.

Nonlinearity (in %) can be calculated with Equation (1):

$$E_{LIN} = \frac{Max(V_{OUT}(I_P) - V_{LIN}(I_P))}{V_{OUT}(I_{PMAX}) - V_{OUT}(-I_{PMAX})} \times 100 \quad (1)$$

Where $V_{LIN}(I_P)$ is the approximate straight line calculated by the least square method. Note that depending on the curvature of $V_{OUT}(I_P)$, E_{LIN} can be positive or negative.

Total Output Error (E_{TOT})

E_{TOT} (in %) is the relative difference between the sensor output and the ideal output at a given primary current (I_P). E_{TOT} can be estimated with Equation (2):

$$E_{TOT}(I_P) = \frac{V_{OUT}(I_P) - V_{OUT_IDEAL}(I_P)}{SENS \times I_P} \times 100 \quad (2)$$

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Where V_{OUT_IDEAL} can be calculated with Equation (3):

$$V_{OUT_IDEAL}(I_P) = \frac{VCC}{2} + SENS \times I_P \quad (3)$$

The total output error incorporates all error sources, and is a function of I_P. At currents close to I_{PMAX}, E_{TOT} is affected mainly by the sensitivity error. At currents close to 0A, E_{TOT} is mostly caused by the offset voltage (V_{OE}). Note that when I_P = 0A, E_{TOT} diverges to infinity due to the constant offset.

Ratiometry Coefficients

Generally, the sensor output is ratiometric. This means that the sensitivity and the zero current output scale with VDD. The ratiometry coefficients measure if the sensitivity and zero output current are proportional.

K_{SENS} can be estimated with Equation (4):

$$K_{SENS} = \frac{SENS(VCC)/SENS(3.3V)}{VCC/3.3V} \quad (4)$$

K_{VO} can be calculated with Equation (5):

$$K_{VO} = \frac{V_{OUT}(I_P = 0V \text{ to } VCC)/V_{OUT}(I_P = 0V \text{ to } 3.3V)}{VCC/3.3V} \quad (5)$$

It is recommended for K_{SENS} and K_{VO} to be equal to 1.

Power On Time (t_{PO})

The power on time (t_{PO}) is the time interval after power is first applied to the device, until the output can correctly indicate the applied primary current. t_{PO} is defined as the time between the following moments:

- t1: The supply reaches the minimum operating voltage (VCC_{MIN}).
- t2: V_{OUT} settles within ±10% of its steady state value under an applied primary current (see Figure 5).

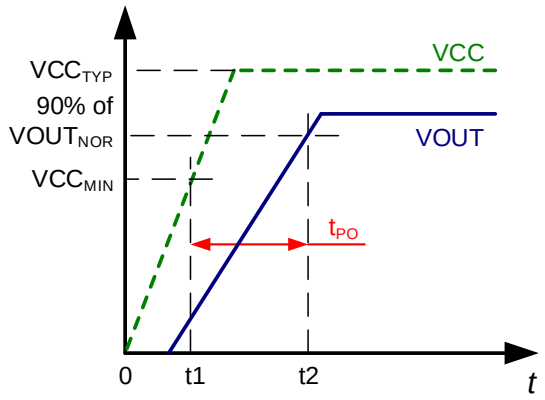


Figure 5: Power On Time (t_{PO})

Propagation Delay (t_{PD})

The propagation delay (t_{PD}) represents the internal latency between an event that has been measured and the sensor's response. t_{PD} is defined as the time between the following moments:

1. t1: The primary current signal reaches 20% of I_{P_MAX}.
2. t2: V_{OUT} reaches 20% of V_{OUTMAX} (see Figure 6).

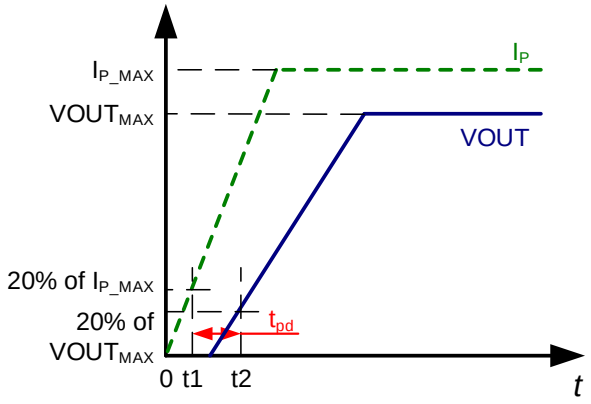


Figure 6: Propagation Delay (t_{PD})

Rise Time (t_R)

The rising time (t_R) is defined as the time between the following moments:

1. t1: The sensor's V_{OUT} reaches 10% of its full scale value.
2. t2: The sensor's V_{OUT} reaches 90% of its full scale value (see **Error! Reference source not found.**).

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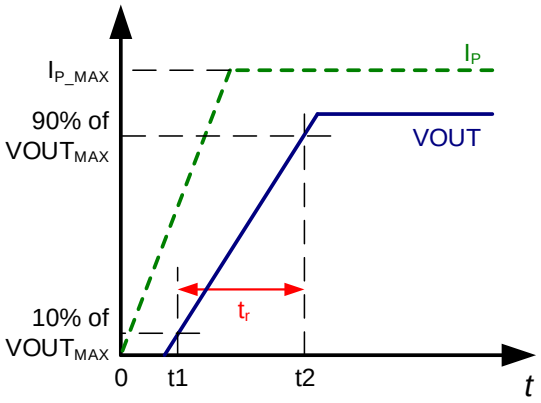


Figure 7: Rising Time (t_R)

The sensor bandwidth (f_{BW}) is defined as the 3dB cutoff frequency.

By using the rising time, f_{BW} can be estimated with Equation (6):

$$f_{BW} = 0.35 / t_R \quad (6)$$

Response Time ($t_{RESPONSE}$)

The response time ($t_{RESPONSE}$) is defined as the time between the following moments:

1. t1: The primary current signal reaches 90% of its final value.
2. t2: V_{OUT} reaches 90% of its output, as it corresponds to the applied primary current (see Figure 8).

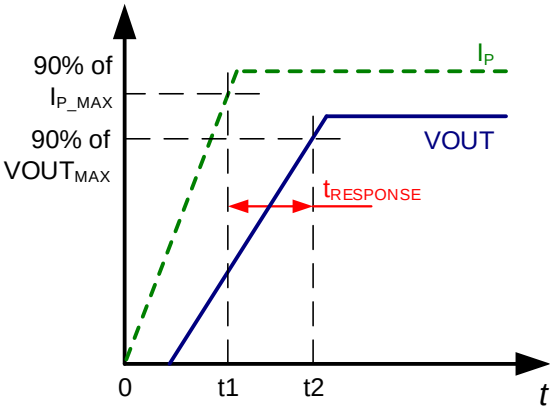


Figure 8: Response Time ($t_{RESPONSE}$)

Remove the ground and power planes under the IC to reduce the effect of eddy currents on t_R and $t_{RESPONSE}$.

Adjustable Bandwidth

The sensor dynamic can be adjusted with an external capacitor (C_F). In this scenario, the bandwidth (f_{BW}) can be calculated with Equation (7):

$$f_{BW} = \frac{1}{2\pi} \times \frac{1}{R_{Fi} \times (C_{Fi} + C_F)} \quad (7)$$

Figure 9 shows the typical bandwidth curve.

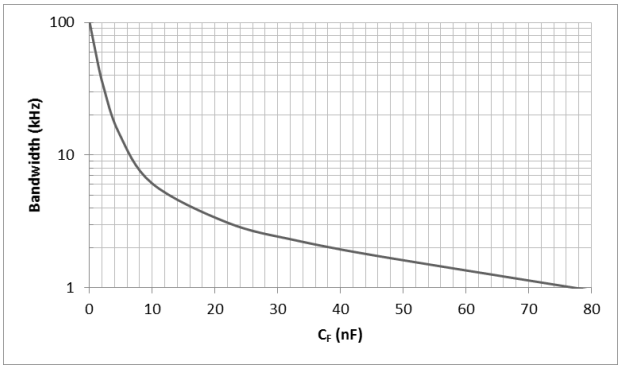


Figure 9: Bandwidth vs. C_F

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APPLICATION INFORMATION

Self-Heating Performance

The conductor and MCS1800 temperatures can rise when current flows through the primary conductor. This means that self-heating should be carefully verified to ensure that the IC junction temperature does not exceed the maximum value (see the Absolute Maximum Ratings on page 5).

The thermal behavior strongly depends on the thermal environment of the IC, as well as its cooling capacity. In particular, thermal behavior depends on the PCB copper area's thickness. The thermal response is also related to the current waveform's profile (e.g. the amplitude and frequency of an AC current, or the peaks and duty cycle of a pulsed DC current).

Figure 10 shows the self-heating performance of the MCS1800 with a DC current input. The data is collected when the MCS1800 is mounted on the device's evaluation board and T_A = 25°C. Values were taken 10 minutes after a continuous current.

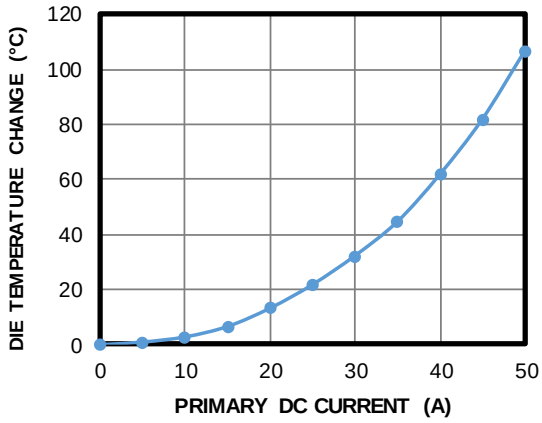
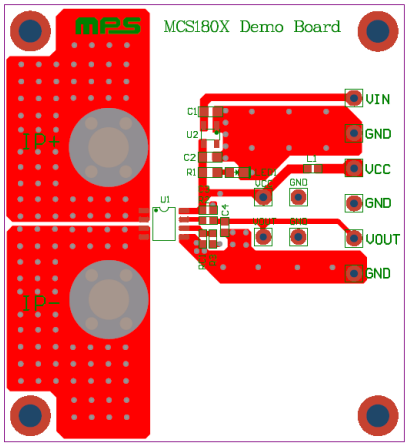
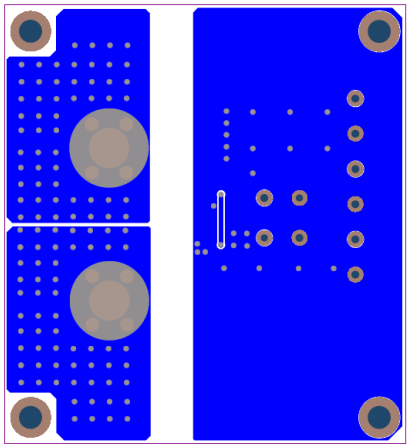


Figure 10: Self-Heating Performance with DC Current Input

Figure 11 shows the top and bottom layers of the PCB. The board includes a total of 2200mm², 4oz (139µm) copper connected to the primary conductor by the IP+ and IP- pins. The copper covers both the top and bottom sides, and thermal vias connect the two layers.



Top Layer



Bottom Layer

Figure 11: Recommended PCB Layout

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TYPICAL APPLICATION CIRCUITS

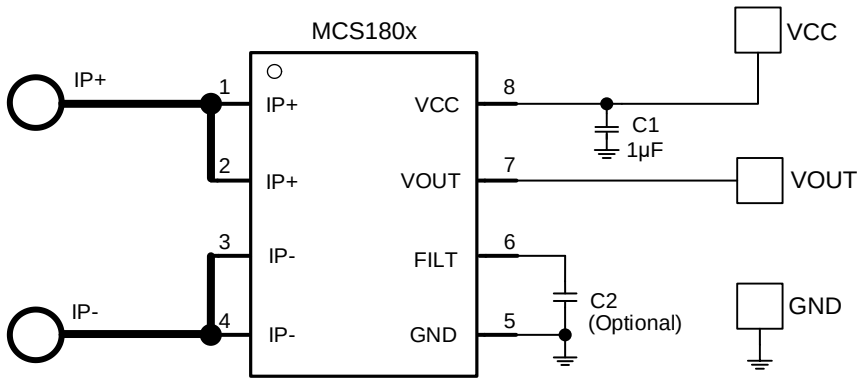
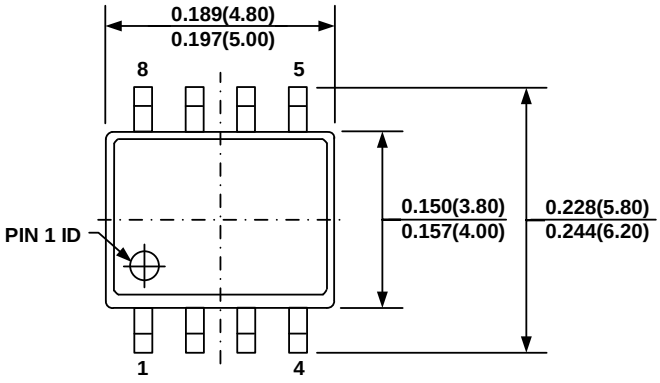


Figure 12: Typical Application Circuit

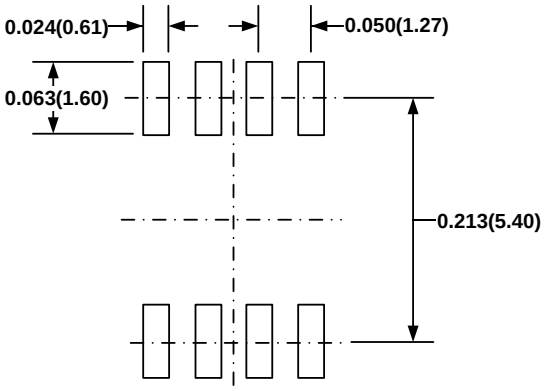
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PACKAGE INFORMATION

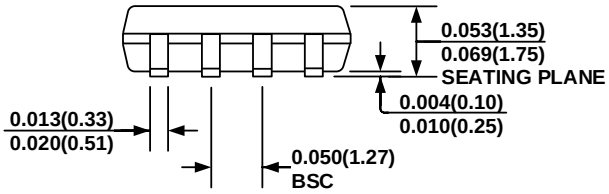
SOIC-8



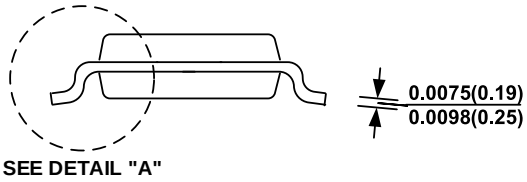
TOP VIEW



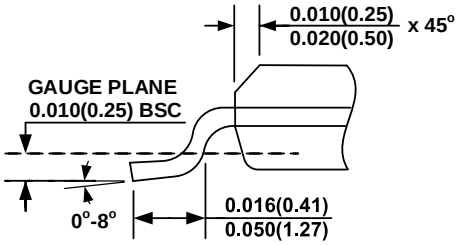
RECOMMENDED LAND PATTERN



FRONT VIEW



SIDE VIEW



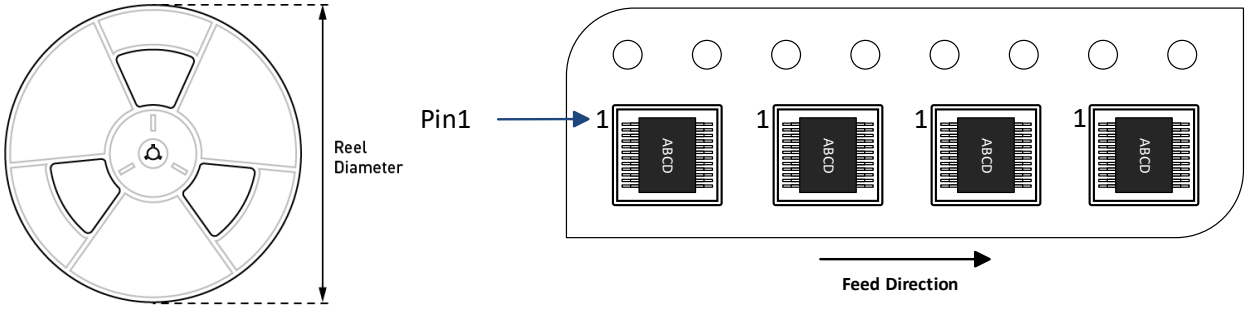
DETAIL "A"

NOTE:

1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSION, OR GATE BURR.
3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES MAX.
5) DRAWING CONFORMS TO JEDEC MS-012, VARIATION AA.
6) DRAWING IS NOT TO SCALE.

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CARRIER INFORMATION



Part Number	Package Description	Quantity/ Reel	Quantity/ Tube	Quantity/ Tray	Reel Diameter	Carrier Tape Width	Carrier Tape Pitch
MCS1800GS-12–Z	SOIC-8	2500	N/A	N/A	13in	12mm	8mm
MCS1800GS-25–Z	SOIC-8	2500	N/A	N/A	13in	12mm	8mm

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REVISION HISTORY

Revision #	Revision Date	Description	Pages Updated
1.0	4/13/2021	Initial Release	-

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