MP3360

Highly Integrated Photo Flash Charger and IGBT Driver

The Future of Analog IC Technology

DESCRIPTION

The MP3360 is a fast, highly efficient and precision high voltage photo-flash charger for camera phone xenon flash.

The MP3360 has programmable peak currents of 1.5A/1.4A/1.3A/1.2A/1.1A/1.0A/0.9A/0.8A /0.75A/0.7A/0.65A/0.6A/0.55A/0.5A/0.45A/0.4A. A 60V, 0.5Ω internal power switch lowers transformer turns ratio and switching losses associated with the primary leakage inductance and winding capacitance. Integrated secondary feedback resistors provide +/-2.5% output voltage charge accuracy. MP3360 also has an integrated IGBT driver with a regulated 2.7V gate voltage.

MP3360 is available in the 10-pin, 2X2 flip chip package.

FEATURES

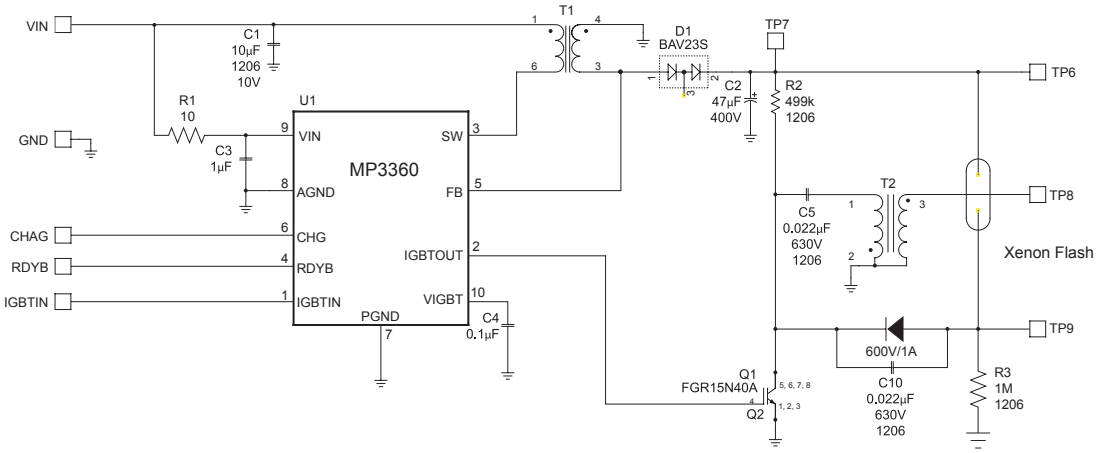
- Integrated 60V, 0.5Ω Power Switch
- Single wire digital interface for current limit programming.
- 2.5% Charge Accuracy
- <1uA Shutdown Current
- Integrated IGBT Driver with Regulated Gate Voltage

APPLICATIONS

- Mobile Phones with Xeon Flash
- Digital Still Cameras
- Optical Film Cameras
- PDAs with Xeon Flash

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TYPICAL APPLICATION



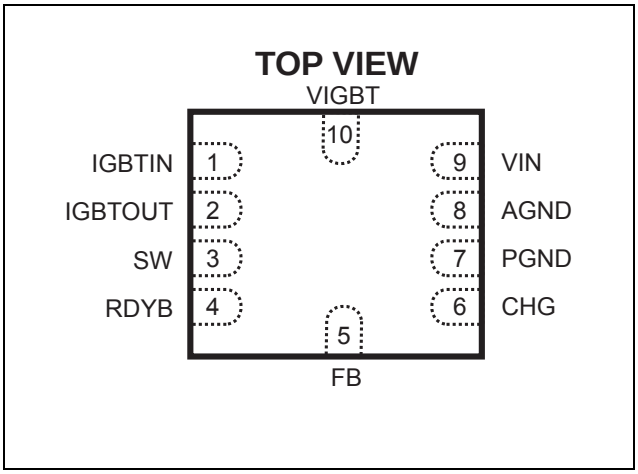
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ORDERING INFORMATION

Part Number*	Package	Top Marking	Free Air Temperature (T_A)
MP3360	10-pin, 2x2 Flip QFN	4KY	-40°C to +85°C

For Tape & Reel, add suffix –Z (e.g. MP3360DG–Z);
For RoHS Compliant Packaging, add suffix –LF(e.g. MP3360DG–LF–Z)

PACKAGE REFERENCE



ABSOLUTE MAXIMUM RATINGS (1)

V_{IN} to GND .....-0.3Vto 6V
CHG, IGBTIN, IGBTOUT, VIGBT,
RDYB to AGND .....-0.3Vto 6V
FB to AGND.....-60V to 350V
SW to AGND.....-0.3V to 60V
PGND to AGND .....-0.3V to +0.3V
2X2, 10 pin Flip Chip Thermal Resistance
Operating Temperature Ranges .....
..... -20°C to +85°C
Storage Temperature..... -55°C to +150°C
Junction Temperature..... +150°C
Continuous Power Dissipation (T_A = +25°C) (2)
.....1.6W
Lead Temperature (Solder).....+260°C

Recommended Operating Conditions (3)

Supply Voltage V_{IN} .....2.5V to 6V
Operating Junct. Temp (T_J)..... -40°C to +125°C

Thermal Resistance (4)	θ_{JA}	θ_{JC}
2x2 Flip Chip	80	16 °C/W

Notes:

1) Exceeding these ratings may damage the device.
2) The maximum allowable power dissipation is a function of the maximum junction temperature T_J (MAX), the junction-to-ambient thermal resistance θ_{JA}, and the ambient temperature T_A. The maximum allowable continuous power dissipation at any ambient temperature is calculated by P_D (MAX) = (T_J (MAX)-T_A)/θ_{JA}. Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
3) The device is not guaranteed to function outside of its operating conditions.
4) Measured on JESD51-7, 4-layer PCB.

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ELECTRICAL CHARACTERISTICS

V_{IN} = V (CHG) = 3.6V, T_A = +25°C, unless otherwise noted.

Parameter	Conditions	Min	Typ	Max	Unit
Photoflash Capacitor Charger					
V_{IN} Voltage Range		2.5		6	V
V_{IN} UVLO	Rising edge, hysteresis = 200mV typical			2.5	V
V_{IN} Quiescent Current	V(CHG)=High, V(SW) = 0		1	2	mA
V_{IN} Quiescent Current	V(CHG)=High, V(FB) = 325V			150	µA
Shutdown Current from V_{IN}	V(CHG)=Low, V_{IN} =3.6V			1	µA
V_{SW} Leakage Current	V_{IN}=3.6V, V_{SW}=60V, in Shutdown			1	µA
SW ON Resistance between SW and GND	Switch turn-on		0.5		Ω
Pull-down Resistance of CHG pin	V(CHG)=3.6V		100		kΩ
IPEAK1	Program for Highest Current Limit	1.3	1.5	1.7	A
IPEAK2	Program for Lowest Current Limit		0.4	0.6	A
Charge completion detect voltage at FB		290	298	308	V
FB Resistance	V(FB)=30V		303		kΩ
DCM Comparator threshold			5		V
RDYB Leakage Current	V(RDYB)=3.6V			0.1	µA
RDYB Output Low Voltage	ISINK= 2mA		0.2		V
MAX TON	Maximum TON time		70	120	µS
Thermal Shutdown	Rising edge, hysteresis = 15°C		150		°C
Charge Input High Voltage		2.4			V
Charge Input Low Voltage				0.6	V
t_{HI}		0.1			µS
t_{LO}		0.1			µS
$t_{EN\ delay}$		30			µS
t_{PW}	Vin=2.7V-5.5V			60	µS

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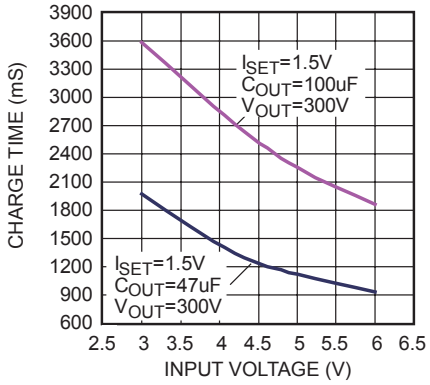
PIN FUNCTIONS

Pin	Name	Function
1	IGBTIN	Logic Input Pin for IGBT Drive.
2	IGBTOUT	Output Drive for IGBT Gate. Connect this pin to the gate of the IGBT.
3	SW	Switch Pin. This is the drain of the internal power switch.
4	RDYB	Open-Drain Power-Ready Output. RDYB becomes low when the output voltage is reached.
5	FB	Feedback Pin. Its trip voltage is 298V
6	CHG	Charge Enable Pin. A low to high transition on this pin puts the part into power delivery mode. Once the target voltage is reached, the part will stop charging the output. Toggle this pin will start charging again. Bring this pin low will terminate the power delivery and put the part in shutdown. This pin can be also used to program the peak current regulation. See the description for current programming.
7	PGND	Power Ground
8	AGND	Analog ground. Tie it directly to local ground plane.
9	VIN	Input Supply Pin. Connect it to system supply voltage. Bypass VIN to GND with a 0.1uF or greater ceramic capacitor.
10	VIGBT	2.7V LDO output. VIGBT is the supply voltage for IGBT gate driver. Bypass VIGBT with 0.1uF cap to GND.

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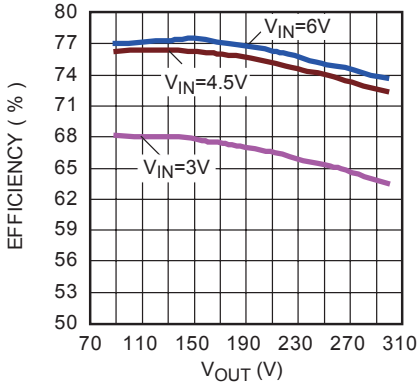
TYPICAL PERFORMANCE CURVES

Charge Time vs. Input Voltage

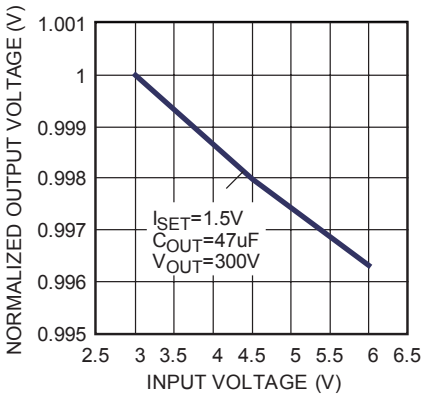


Efficiency vs V_{OUT}

I_{SET}=1.5A

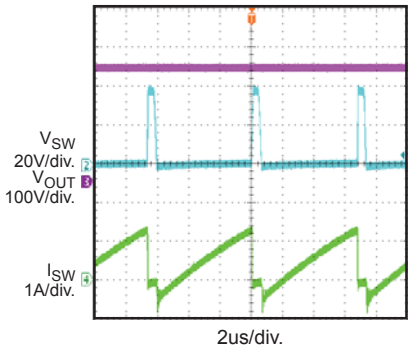


Line Regulation



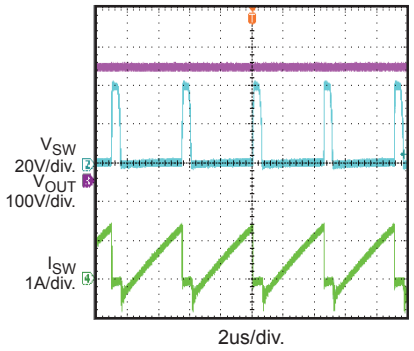
Switching Waveform

V_{IN} = 3.3V, V_{OUT} = 300V,
I_{SET} = 1.5A, C_{OUT} = 47uF



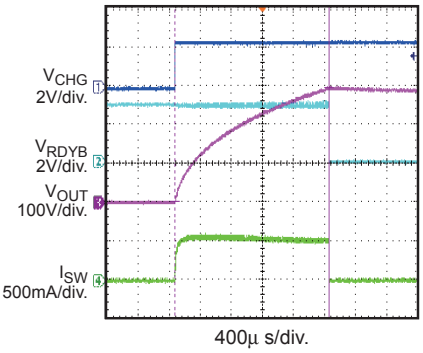
Switching Waveform

V_{IN} = 5.0V, V_{OUT} = 300V,
I_{SET} = 1.5A, C_{OUT} = 47uF



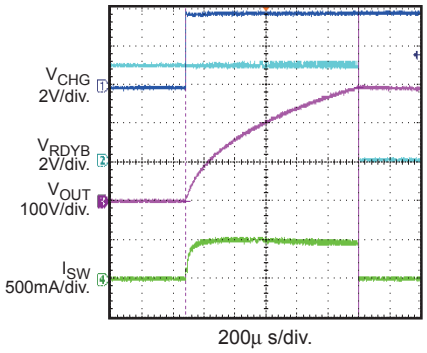
Charging Waveform

V_{IN} = 3.3V, V_{OUT} = 300V,
I_{SET} = 1.5A, C_{OUT} = 47uF



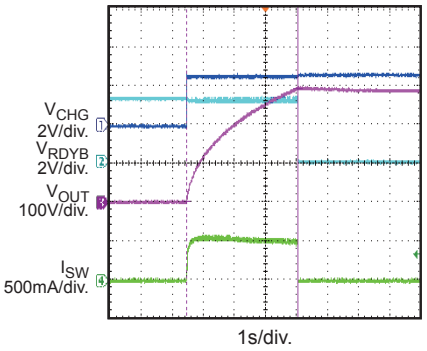
Charging Waveform

V_{IN} = 5.0V, V_{OUT} = 300V,
I_{SET} = 1.5A, C_{OUT} = 47uF



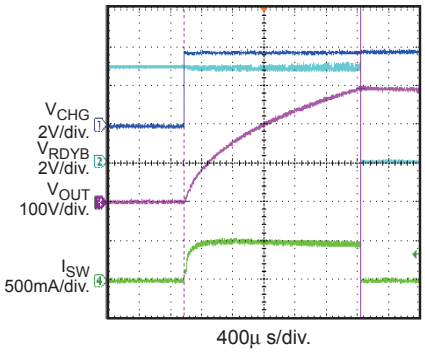
Charging Waveform

V_{IN} = 3.3V, V_{OUT} = 300V,
I_{SET} = 1.5A, C_{OUT} = 100uF



Charging Waveform

V_{IN} = 5.0V, V_{OUT} = 300V,
I_{SET} = 1.5A, C_{OUT} = 100uF



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BLOCK DIAGRAM

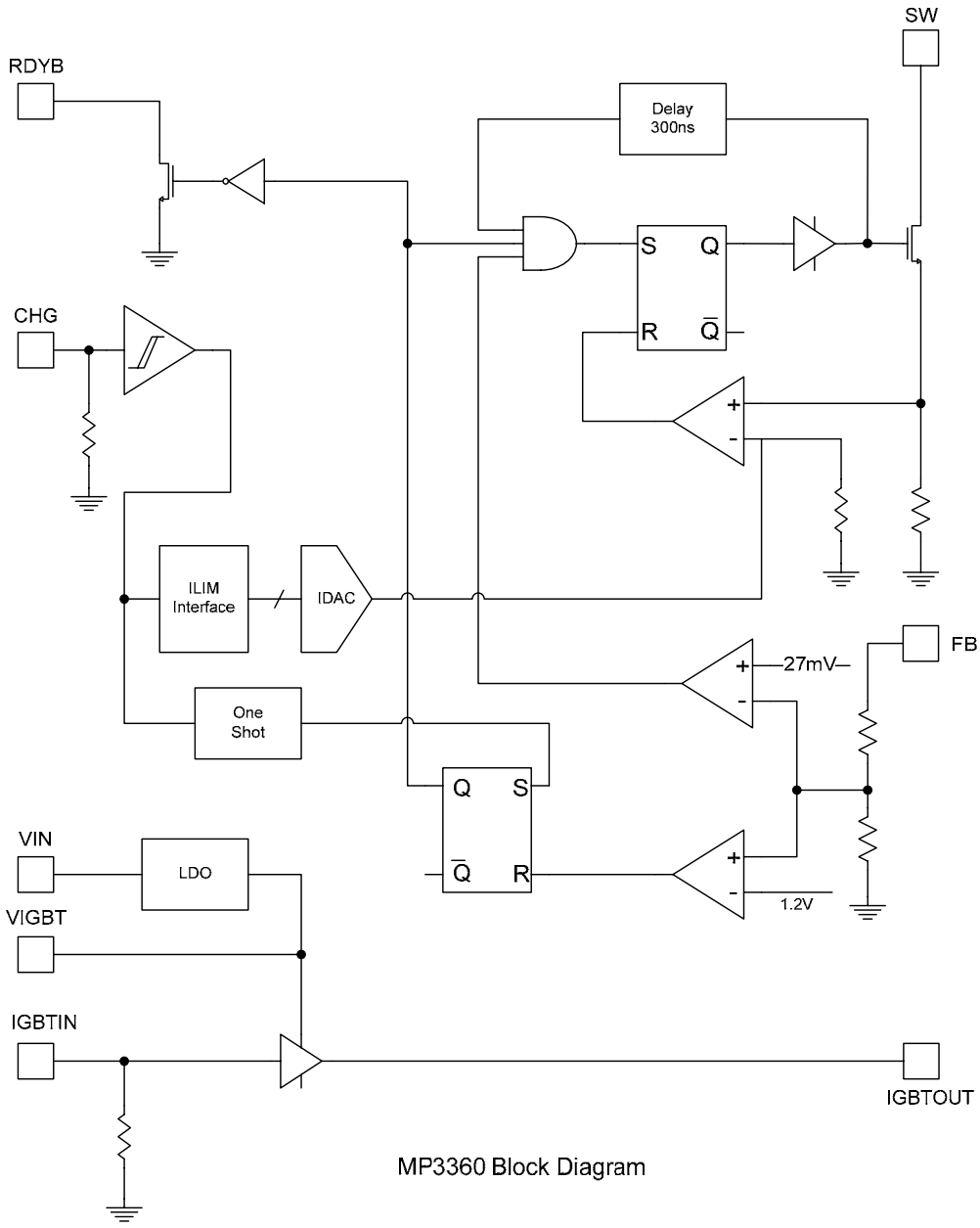


Figure 1—Functional Block Diagram

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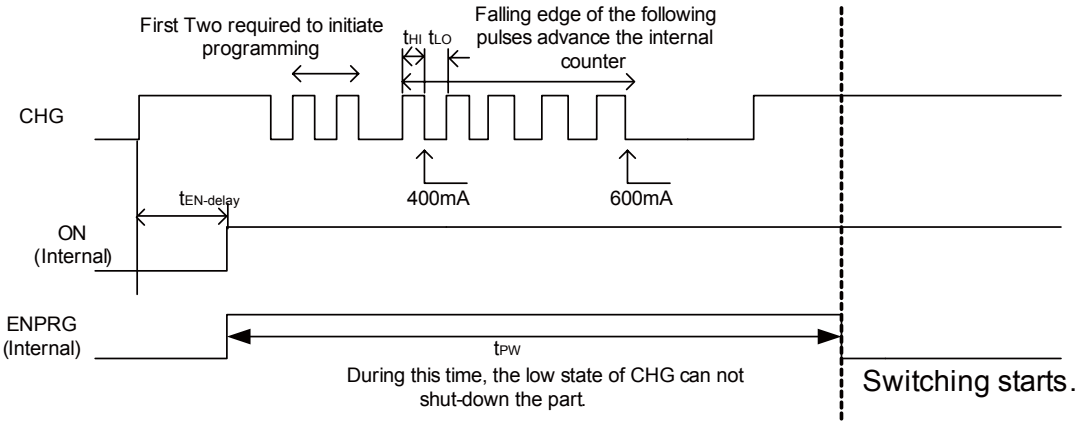
OPERATION

The MP3360 controlled flyback charger operates in critical conduction mode with peak current set by the CHG pin. The output voltage is divided down through internal 300: 1 resistive divider from the positive terminal of the transformer secondary (FB pin) and compares it with the internal 1.2V reference. The low to high transition of the CHG pin will enable the flyback converter to switch.

A constant T_{OFF} of 20µs is used when the output voltage is below 20V to avoid inrush current. The boundary mode operation will follow to minimize charge time when the output voltage is above 20V. A minimum T_{OFF} of 200ns serves as blanking for turn off transition. The circuit will stop switching and RDYB will be pulled low once the flash capacitor is charged to 300V, a value set by the internal 300:1 R divider and 1.2V reference. VIGBT will be regulated to 2.7V. When the charge is full, then part will shut down its internal circuitry with less than 1uA drawn from V_{IN}. Toggling the CHG pin will restart charge cycle. Bringing the CHG pin low terminates the power delivery and put the part in shutdown. A maximum T_{ON} timer prevents pulling current from a depleted battery. If the ON time exceeds maximum T_{ON}, the switch is forced OFF regardless of the I_{PEAK} detection.

CURRENT LIMIT PROGRAMMING

The current limit can be programmed using the CHG pin. After asserting the CHG, it should be held high for at least 30us. After that, it takes two pulses to enter the programming mode. In the programming mode, each pulse advances the internal counter to the next higher current limit by 50mA until 750mA is reached and by 100mA increments beyond 750mA. The first pulse is for 400mA current limit. The next pulse will set the current limit to 450mA, etc. This programming should be done within 60us. Otherwise, the programming pulses will be ignored after the programming window expires. When you have advanced to the desired current limit setting, the high state should be held to make the part switch with the set current limit. To set the current limit to 1.5A (highest possible), the previous procedure is not necessary. Asserting and holding high the CHG pin will make the part switches with a 1.5A current limit. See the following timing diagram for detailed timing information.

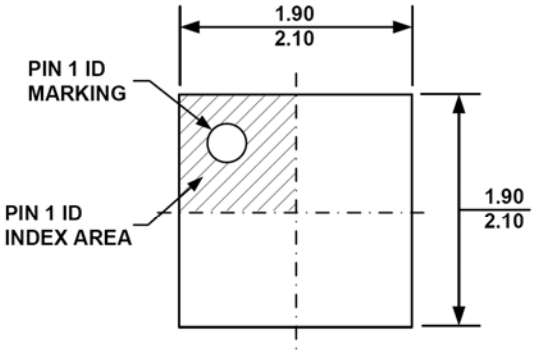


ILIM Programming Timing Diagram
(600mA Current Limit)

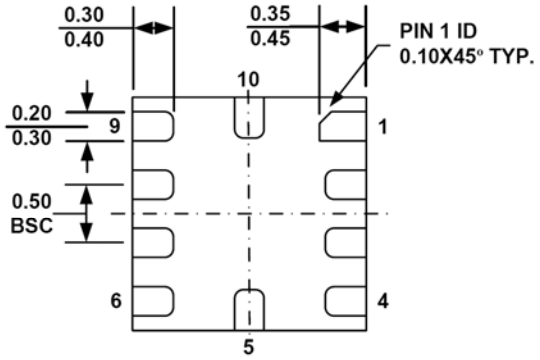
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PACKAGE INFORMATION

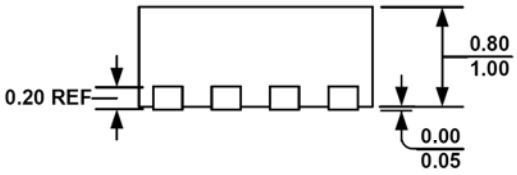
QFN10 (2mm x 2mm)



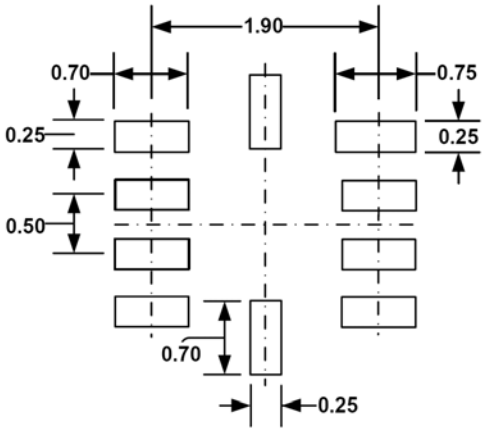
TOP VIEW



BOTTOM VIEW



SIDE VIEW



RECOMMENDED LAND PATTERN

NOTE:

1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) EXPOSED PADDLE SIZE DOES NOT INCLUDE MOLD FLASH.
3) LEAD COPLANARITY SHALL BE 0.10 MILLIMETER MAX.
4) JEDEC REFERENCE IS MO-220, VARIATION VCCD.
5) DRAWING IS NOT TO SCALE.

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